

Amendments to the Claims

This listing of the Claims will replace all prior versions and listings of the claims in this patent application.

Listing of the Claims

Claims 1-43 (canceled)

44. (previously presented) A circuit component comprising:

a substrate;

a semiconductor chip over a top surface of said substrate, wherein said semiconductor chip has a front surface facing said top surface of said substrate and a back surface opposite to said front surface, and wherein said semiconductor chip comprises multiple pads at said front surface;

an identity of product directly on said back surface of said semiconductor chip;

multiple metal bumps between said multiple pads of said semiconductor chip and said top surface of said substrate; and

an optically transparent layer directly over said identity of product, wherein said identity of product is visible through said optically transparent layer.

Claims 45-47 (canceled)

48. (previously presented) The circuit component of claim 44 further comprising an underfill between said front surface and said top surface of said substrate, wherein said underfill encloses said multiple metal bumps.

49. (previously presented) The circuit component of claim 44 further comprising multiple balls on a bottom surface of said substrate.

Claims 50-59 (canceled)

60. (currently amended) The circuit component of claim 44, wherein said multiple metal bumps comprise a solder.

61. (previously presented) A circuit component comprising:

a substrate;

a semiconductor chip over a top surface of said substrate, wherein said semiconductor chip has a front surface facing said top surface of said substrate and a back surface opposite to said front surface, and wherein said semiconductor chip comprises multiple pads at said front surface;

an identity of manufacturer directly on said back surface of said semiconductor chip;

multiple metal bumps between said multiple pads of said semiconductor chip and said top surface of said substrate; and

an optically transparent layer directly over said identity of manufacturer, wherein said identity of manufacturer is visible through said optically transparent layer.

62. (previously presented) The circuit component of claim 61 further comprising an underfill between said front surface and said top surface of said substrate, wherein said underfill encloses said multiple metal bumps.

63. (previously presented) The circuit component of claim 61 further comprising multiple balls on a bottom surface of said substrate.

64. (currently amended) The circuit component of claim 61, wherein said multiple metal bumps comprise a solder.

65. (previously presented) A circuit component comprising:

a substrate;

a semiconductor chip over a top surface of said substrate, wherein said semiconductor chip has a front surface facing said top surface of said substrate and a back surface opposite to said front surface, and wherein said semiconductor chip comprises multiple pads at said front surface;

a bar code directly on said back surface of said semiconductor chip;

multiple metal bumps between said multiple pads of said semiconductor chip and said top surface of said substrate; and

an optically transparent layer directly over said bar code, wherein said bar code is visible through said optically transparent layer.

66. (previously presented) The circuit component of claim 65 further comprising an underfill between said front surface and said top surface of said substrate, wherein said underfill encloses said multiple metal bumps.

67. (previously presented) The circuit component of claim 65 further comprising multiple balls on a bottom surface of said substrate.

68. (currently amended) The circuit component of claim 65, wherein said multiple metal bumps comprise a solder.